

Lecture 10 - DEBUGGING

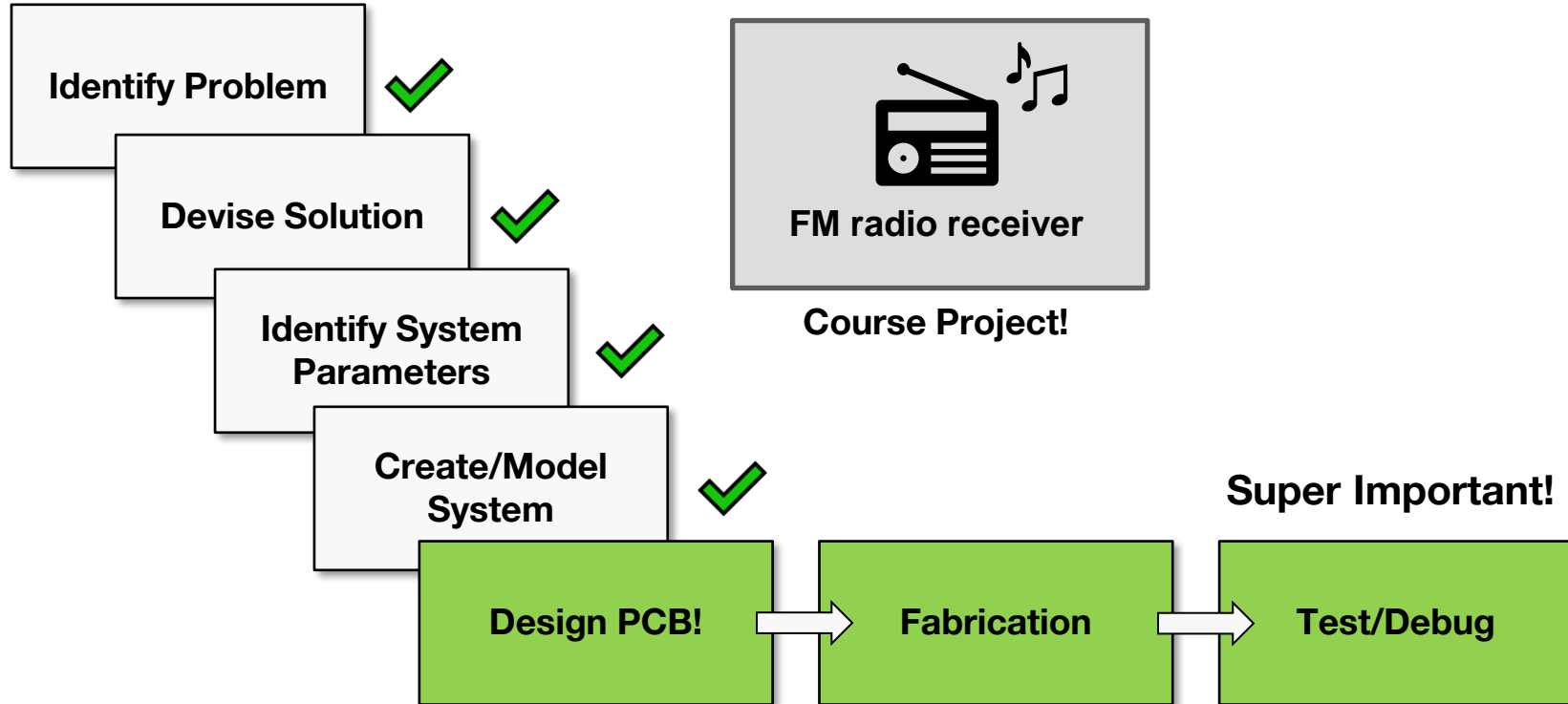
Identifying and Solving Circuit Issues

Outline

- Introduction
- Tools for Debugging
- Debugging Strategies
- Causes Behind Bugs
- Fixing Issues
- Summary

System Design

Course Project



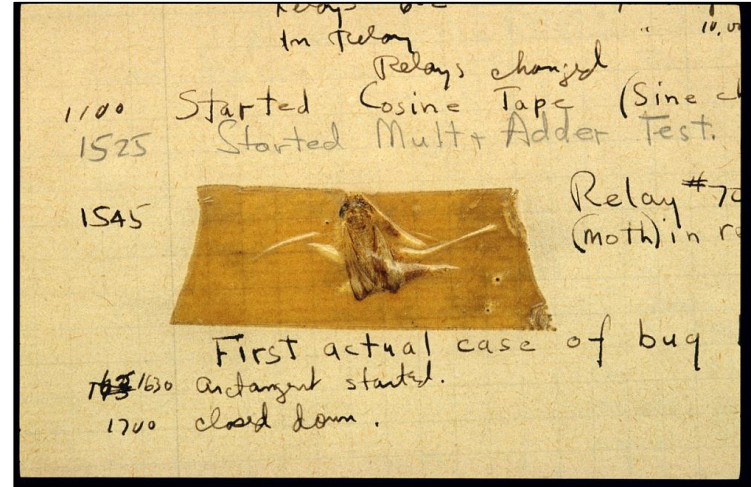
Introduction

What exactly is debugging?

The term was popularized by Admiral Grace Hopper when she pulled a moth out of the Harvard Mark II in the 1940s

Didn't we already spend so much time being meticulous with our design?

- We're never perfectly sure what's going on
- Models are never a fully accurate representation of real-world operation
- Tolerances and parasitics can be different in reality than what the datasheet says, or have a greater impact than we expect
- We probably didn't model everything



Introduction

A Bit on Debugging

Debugging is hard...but it's super rewarding!

Through our design process, we have learned a lot about how our circuits are supposed to function, and now we get to apply that knowledge

It's not as scary as it looks

- **Many common errors that are easily fixable**
- **Many tools to help us**



Tools for Debugging

Multimeters

Our Best Friend

Multimeters are easy to use and have a ton of functionality – they should be your first stop

Voltage

- Potential difference between the two probes

Resistance/Diode/Continuity

- Useful to check connectivity of traces and make sure we don't have shorts
- Make sure that there is nothing powering the circuit when doing this

Current

- Not super useful for debugging but important in general
- Use cautiously for low currents only



Oscilloscopes

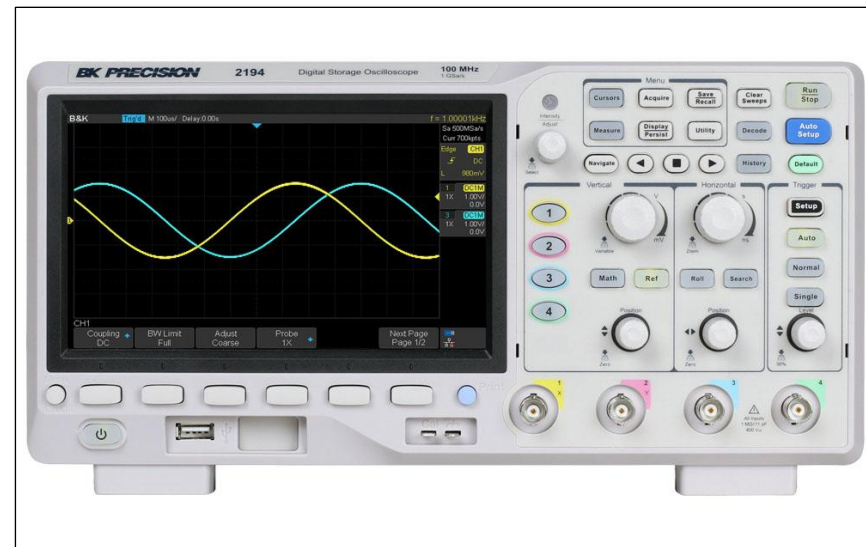
Our Other Best Friend

Oscilloscopes help to actually "see" our signals over time whereas multimeters only give one instantaneous value

Thus, we can use scopes for signals that aren't just constant DC values

Scopes show multiple signals at the same time

- Input vs. output of an IC to make sure it's doing what we expect
- Before and after a bypass capacitor to see how well it's filtering our power rails



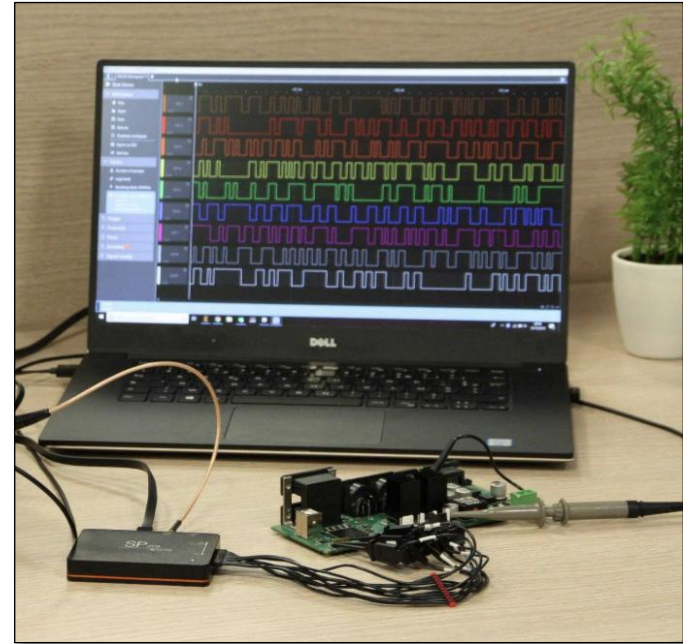
Logic Analyzers

Characterizing Digital Signals

Logic analyzers are basically digital oscilloscopes – they can display several digital signals at the same time

Really good for debugging communication systems and digital timing errors

Shows the values of bits being sent over digital lines



Power Supplies

Injection!

Power supplies allow us to inject a known voltage or current into points on our PCB

Allows us to test the various parts of our circuits without having to power on the entire board and risk damaging any components

We can set current limits and voltage limits to control the amount of power we inject – we won't destroy our board if there is a short

Ex: testing a power regulator

- To ensure that the USB power is correctly getting stepped down to 3.3V, we can inject 5V into the USB power trace and probe the output of the buck converter



Debugging Strategies

Continuity Checks

Identifying shorts

1. Ensure that nothing is supplying power to your board (everything is off)
2. Turn the multimeter to continuity mode
 - Represented by propagation waves
 - This will often be combined with the resistance or diode setting
3. Probe across the relevant traces
 - If the multimeter makes a beeping sound or shows a low resistance on the screen, then you have a short
 - If the multimeter is quiet or shows OL or a really high resistance, then your traces are separated



Check Voltages

Probing is super helpful

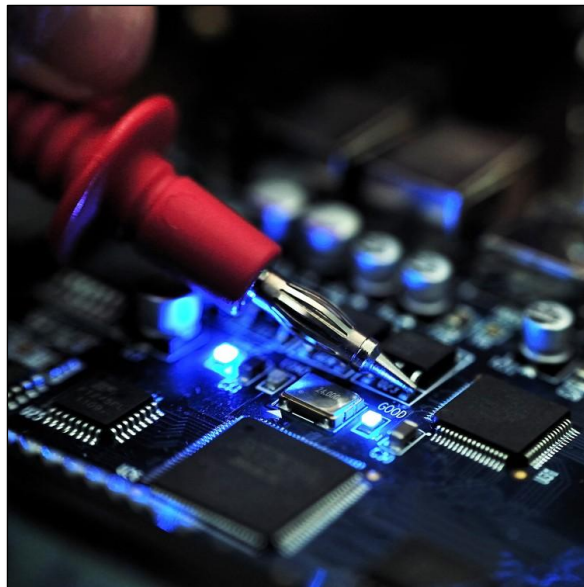
Ensure that all of the power traces are at the correct DC voltages

Ensure that all ICs are getting the correct input power and input signals

Ensure that the output of the ICs are what is expected given their input

- A lot of this can be done with a multimeter, but for more complex signals, don't be scared to use an oscilloscope or logic analyzer

PROBE EVERYTHING!!

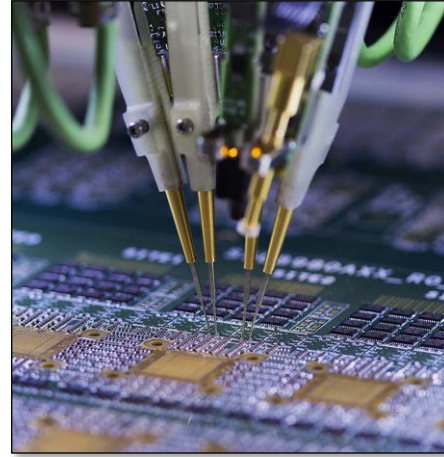


Industrial Debugging Techniques

Because they're cool

Flying Probe Machines

- Consists of several test probes that automatically probe test points on a PCB
- Used for earlier-stage prototyping and low-volume production



In-Circuit Testers

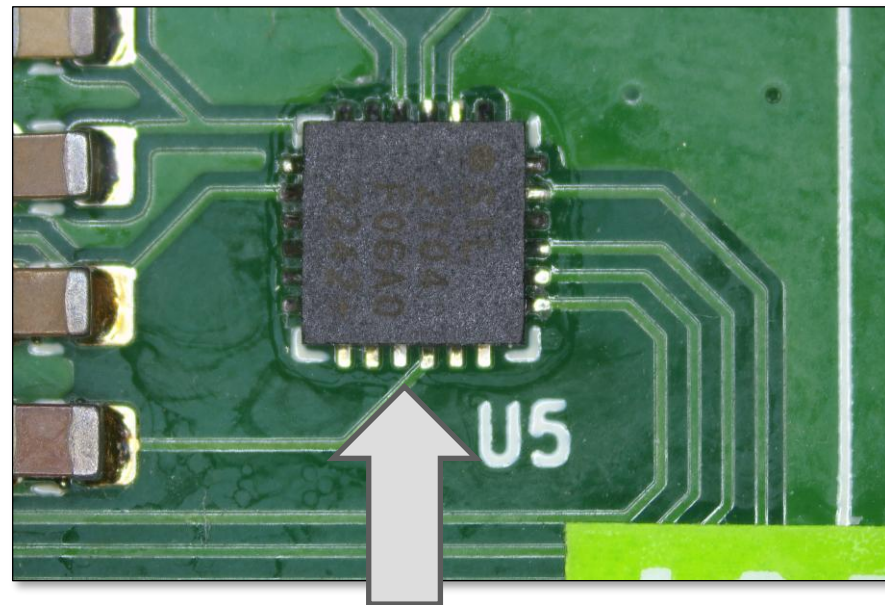
- Consists of a custom bed-of-nails test fixture that attaches to a PCB's test points
- Performs various tests in a given sequence including continuity/shorts, passives measurements, etc.
- Used for later-stage testing and high-volume production



Causes Behind Bugs

Too Little Solder

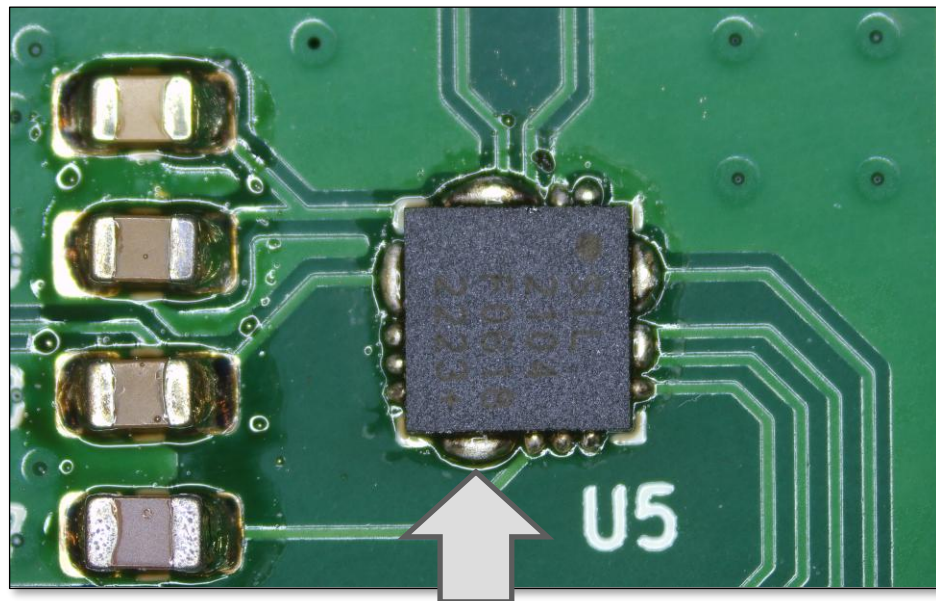
The bottom row of pins on U5 is not properly soldered, indicated by the gold color of the pads as opposed to the silver color of reflowed solder



Gold-colored ENIG pads still visible (no enough solder)

Too Much Solder

Excess solder leads to bridges between pads that should be isolated, leading to shorts



Short between multiple pins
due to excess solder

Incorrect Components

Sometimes there are sourcing issues or small design errors that lead to a board malfunctioning

Ex: Boost converter circuit

- Output voltage is determined by the ratio of R1 and R2
- In this case, given an R3 of 300k, R4 should be 10.6k to get an output of 36V
- If a well-meaning designer or assembler rounds this down to 10k, the output voltage becomes 38V – if other components were only sized for 36V, they could be damaged

Datasheet Snippet

9.2.2.1 Program Output Voltage

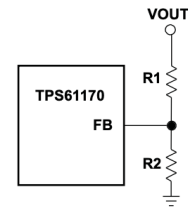


Figure 13. Program Output Voltage

To program the output voltage, select the values of R1 and R2 (see Figure 13) according to Equation 2.

$$V_{out} = 1.229 \text{ V} \times \left(\frac{R1}{R2} + 1 \right)$$

$$R1 = R2 \times \left(\frac{V_{out}}{1.229 \text{ V}} - 1 \right)$$

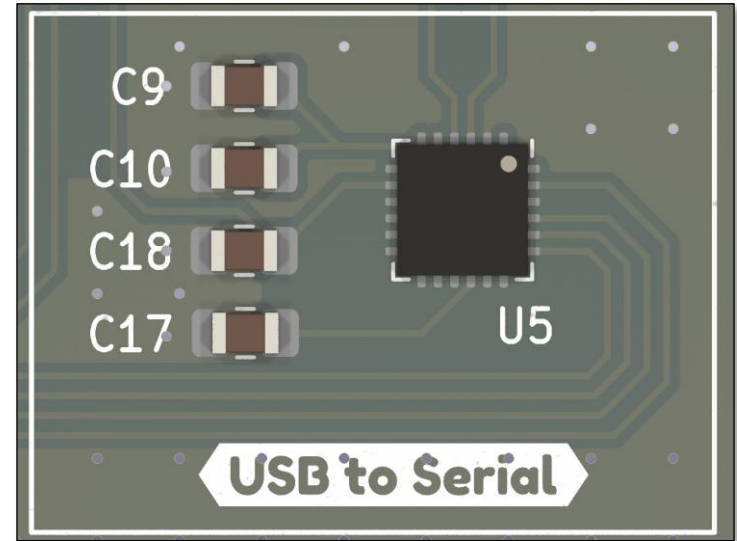
Incorrectly Placed Parts

Easy to mess up orientation of non-passive, symmetric components (such as U5 on the right)

- Square-shaped ICs
- Diodes

Alignment dots are wonderful, but sometimes they're really small on the IC or obscured by the silkscreen on the PCB

We can always double check the orientation of our components by looking at our schematic and layout files and the component's datasheet, and then matching traces and pins



Signal Timings

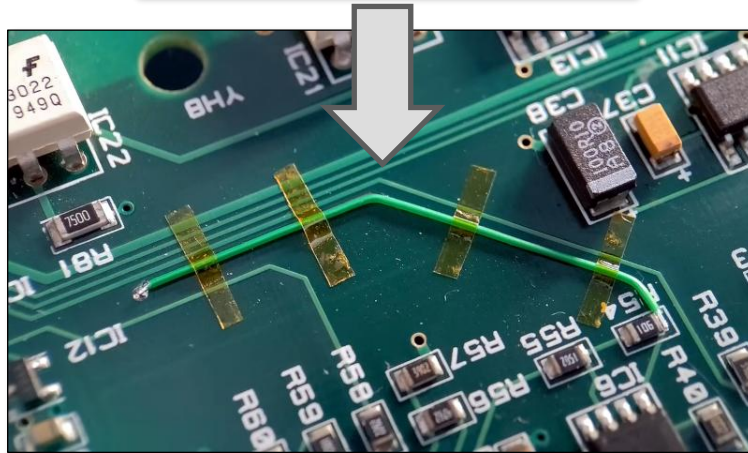
More complex systems with high-speed communication are prone to timing errors

This is oftentimes a result of traces that are improperly sized (either too long or too short) that are attached to RC networks

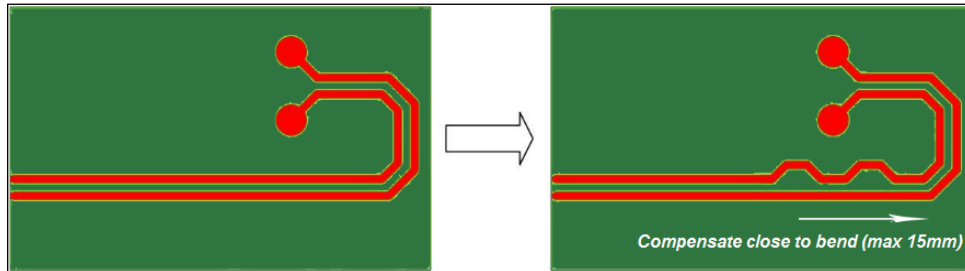
- If the time constant is too high relative to a signal's travel time, an unwanted delay in data transfer occurs
- If the time constant is too low relative to a signal's travel time, needed delay times might not be met

Differential pair mismatches can also yield timing errors in data transfer, such as with USB

Splice trace and add an external wire to lengthen



<https://hackaday.com/2024/07/14/five-ways-to-repair-broken-pcb-traces/>



Fixing Issues

The Easier Problems

Incorrect passive values

- The footprints for passives are generic so it is fairly simple to desolder and resolder a new component with the right value
- Can stack components or solder adjacent components to mimic parallel and series connections for intermediate values

Incorrect IC orientations

- A bit more annoying to fix but can still just be desoldered and resoldered on correctly

Individual solder joints

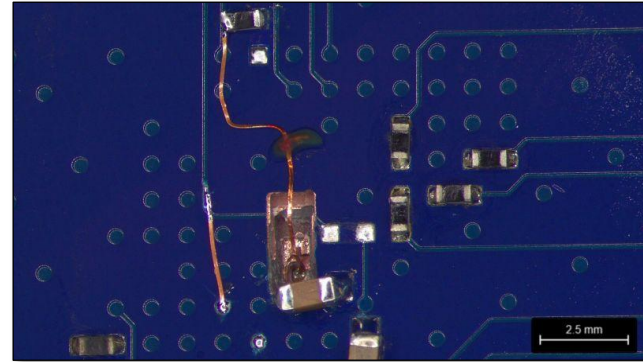
- Can usually be fixed with a hand iron, solder wick, flux, and solder



The Harder Problems

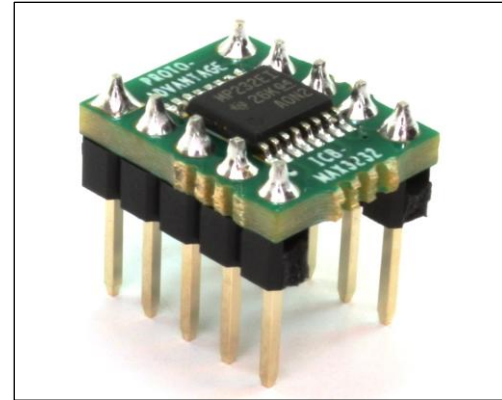
Incorrect Net Connections

- Traces on outer layers can be cut and reconnected using thin magnet wire
- Component connections can be rerouted by soldering wires to the excess space on their pads
- Test points can be utilized for connections



Incorrect Footprints

- Incorrect passive pad sizes can be solved with careful soldering
- If the footprint is missing pins, the remaining pins can be connected using thin magnet wire
- Footprint pad size can be increased by scraping off surrounding soldermask
- IC breakout boards can be used to externally connect to an incorrect footprint via jumper wires



Summary

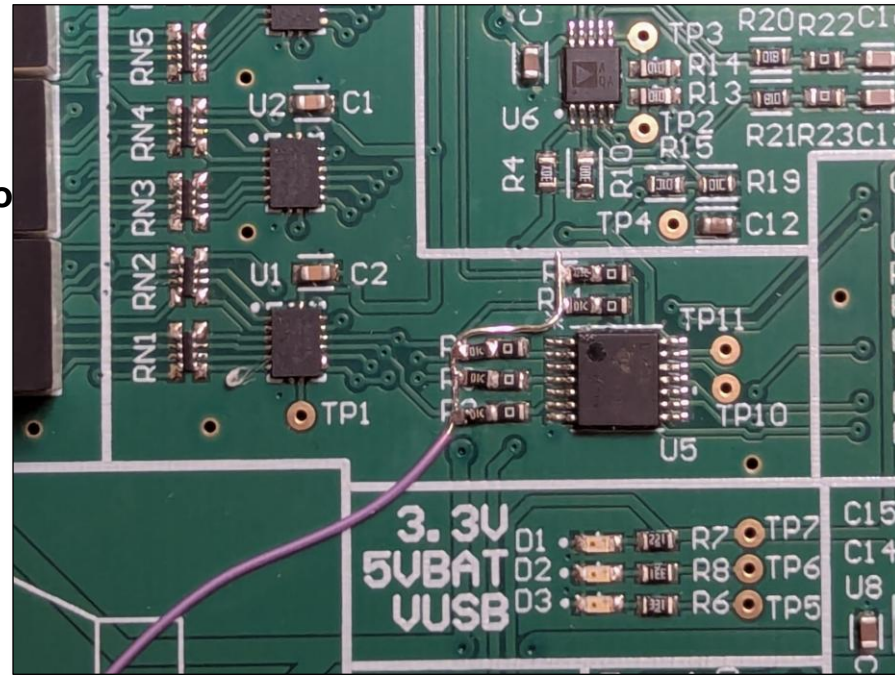
Debugging can be fun!

Debugging can definitely be tedious and frustrating, but it's a really fruitful process!

There are many tools to aid along the way, and to help really understand a board

Some useful resources on debugging:

- [Cadence's Take on Debugging Techniques](#)
- [Board-Level Repair Guide](#)
- [A Really Good Guide to Debugging from a Former 6.204/6.101 TA](#)



Questions

Announcements

- Complete Lecture Quiz 10
- Make-up missed lab check-offs during office hours
- Course evaluations (closes Friday)!
- All office hours (except for TTh 5-7PM) will be in 38-500 (see schedule)
- Extra office hours today from 1-5PM in 38-500